Event Report

Title of the Event: One Week Faculty Development Program on "Artificial Intelligence Drives to Circuit Implementation"
Dates: 1st February 2021 – 6th February 2021
Organized by: Dr. Shijin Kumar P. S & Dr. G. Amanath
Department: Electronics and Communication Engineering (ECE)
Institution: Marri Laxman Reddy Institute of Technology and Management (MLRITM)

The Department of Electronics and Communication Engineering at MLRITM successfully organized a One Week Faculty Development Program titled "Artificial Intelligence Drives to Circuit Implementation" from 1st to 6th February 2021. The FDP was designed to enhance faculty knowledge in bridging artificial intelligence algorithms with practical electronic circuit implementation for real-world engineering solutions.

The program covered AI fundamentals including supervised and unsupervised learning, neural networks, deep learning architectures, and their role in hardware design. Eminent speakers and subject experts delivered insightful sessions focusing on mapping machine learning models to VLSI circuits, neuromorphic computing, FPGA-based implementations, and system-level design considerations.

Hands-on training provided participants with practical exposure to simulation environments and circuit design tools used in hardware realization of intelligent systems. Case studies involving image recognition, edge AI accelerators, and embedded neural network integration enabled participants to understand performance, power optimization, and deployment challenges.

The FDP encouraged the adoption of AI-enabled circuit design approaches to promote innovation in applications related to smart electronics, IoT systems, robotics, and communication devices — areas highly relevant to the ECE curriculum. Participants actively engaged in discussions, Q&A sessions, and collaborative tasks that contributed to an effective knowledge exchange environment.

The event concluded with a valedictory ceremony where certificates were distributed to all participants. The organizers expressed sincere gratitude to MLRITM management, resource speakers, and the ECE faculty team for their continuous support in making the program a remarkable success.

Overall, this FDP strengthened the technical capabilities of faculty members, enabling them to integrate modern AI techniques with hardware design principles and contribute effectively to advanced engineering education and research.